

# TestStation LX™ In-Circuit Test System

## Highest Quality In-Circuit Test

- Configurable from 256 to 7680 test pins
- Capable of testing large, complex, and heavily integrated PCBs
- Safe, accurate, and reliable electrical test with SafeTest™ Protection Technologies
- Powerful CAD preparation and automatic program development software
- Comprehensive fault detection using advanced digital and analog subsystems
- Exceptional diagnostic accuracy
- Designed for fast test throughput



Teradyne's TestStation LX provides accurate, reliable and safe electrical test

The TestStation LX system is a cost-effective in-circuit test solution providing high-volume electronics manufacturers with reliable, high-quality test for the latest PCB technologies. Configurable from 256 to 7680 test pins, it is the highest-capacity in-circuit test solution on the market. Featuring SafeTest protection technologies and a turnkey test programming environment, the TestStation LX system allows direct transfer of test programs and fixtures from all 228X and TestStation in-circuit testers without extra costs or further program development. Additionally, TestStation LH and standard size test systems (3840 pin receiver) fixtures and programs are easily transferred to the TestStation LX platform, saving time and money.

### Comprehensive Fault Detection

Unpowered test capabilities for the TestStation LX system include shorts, vectorless opens, and analog value testing. Powered-up test capabilities include digital device vector testing, reduced-access boundary scan testing, high-speed FLASH and ISP device programming, frequency and time event measurements, synchronized mixed signal device testing, and functional cluster testing. Tests can be generated automatically using Teradyne's D2B CAD preparation software or manually generated using a simple, but powerful test programming language. Teradyne's advanced test quality software analyzes programs to show test fault coverage, and identify potentially unreliable tests that may require additional debug.

### Accurate, Reliable, and Safe Test

SafeTest protection technologies featuring patented UltraPin™ driver/sensor technology assure voltage accuracy and backdrive current measurement to provide accurate, reliable, and safe powered-up testing of today's sensitive low-voltage technologies. Per pin programmable logic levels, backdrive current, and backdrive duration thresholds ensure harmful voltage and currents are not applied to boards during device testing, even on defective boards. Multi-level digital isolation software automatically isolates device outputs on nets being driven, minimizing backdrive conditions and preventing harmful voltage spikes from occurring when backdriven outputs suddenly change logic state. A specialized digital controller minimizes the duration of backdrive currents and reduces the opportunity for voltage spikes occurring from on-board activities.

### Exceptional Diagnostic Accuracy and Test Throughput

Unrivalled diagnostic accuracy is achieved because the closed-loop, low-impedance driver remains accurate even under fault conditions. Additionally, unique programmable backdrive measurement and control capabilities detect faulty enable pins and marginal output transistors not detected on other in-circuit testers. The patented diagnostic algorithms SoftProbe™, BusBust™, and Adaptive Patterns are designed to minimize unnecessary board repairs and eliminate false failure reports.

## Specifications

Base System	<ul style="list-style-type: none"> <li>- Synchronized Analog and Digital Subsystems</li> <li>- Standard (3840 pin) or Large (7680 pin) receiver</li> <li>- All pins have parallel drive and sense capability</li> <li>- Pentium-based PC system</li> <li>- Color LCD monitor on adjustable height platform</li> <li>- Premium software support</li> </ul>	<ul style="list-style-type: none"> <li>- Ethernet Networking Interface (10BASE-T/Thinware)</li> <li>- Hardware Warranty</li> <li>- Automatic Vacuum Control for single and dual well fixtures</li> <li>- System Footprint 66" x 44.5"</li> <li>- Keyboard with integrated mouse</li> </ul>
Analog Hardware	<ul style="list-style-type: none"> <li>- Measurement Matrix: 8 lines by n pins</li> <li>- 2 Sources, configurable as current or voltage</li> <li>- DC Voltage Source: programmable, 16-bit, 0 to <math>\pm 18</math> V over 4 ranges, to <math>\pm 500</math> mA, programmable current limiting</li> <li>- DC Current Source: programmable, 16-bit, 0 to <math>\pm 500</math> mA over 8 ranges, to <math>\pm 18</math> V, programmable voltage limiting</li> <li>- DC Voltmeter: 0 to <math>\pm 200</math> V over 9 ranges</li> <li>- DC Ammeter: 0 to <math>\pm 160</math> mA over 7 ranges</li> <li>- Arbitrary Waveform Generator</li> <li>- Reactance Module               <ul style="list-style-type: none"> <li>• Programmable frequency from 15Hz to 100 kHz</li> <li>• Programmable AC level to 7 Vrms, 12-bit</li> <li>• Programmable DC offset, 16-bit</li> </ul> </li> </ul>	<ul style="list-style-type: none"> <li>- True RMS-DC Detection</li> <li>- Differential Detector/DVM/Digitizer</li> <li>- Coherent Transfer Function Measurement</li> <li>- Component Measurement Capability               <ul style="list-style-type: none"> <li>• Resistive (R) Range: 0.1 to 30 Mohm</li> <li>• Capacitive (C) Range: 1 pF to 10,000 <math>\mu</math>F</li> <li>• Inductive (L) Range: 10 <math>\mu</math>H to 1,000 H</li> </ul> </li> <li>- External Instrument Matrix: 9 BNCs to 8 line to internal instruments or n pins</li> <li>- Traceable Calibration Daughterboard</li> <li>- High Voltage Source configurable as current or voltage, programmable voltage limit, <math>\pm 120</math>V, <math>\pm 50</math>mA</li> <li>- IEEE-488 Interface Controller</li> </ul>
Digital Hardware	<p>Common Driver Characteristics</p> <ul style="list-style-type: none"> <li>- Range: 26 programmable drive levels from + 5.5 V to - 2.5 V</li> <li>- Automatic drive verification at each pin:           <ul style="list-style-type: none"> <li>Four voltages selectable for each pin</li> </ul> </li> <li>- Output Current (with automatic compensation circuitry) &gt; 600 mA;</li> <li>- Programmable Slew Rates 50 - 300 V/<math>\mu</math>S</li> <li>- Typical Output impedance: &lt; 2.0 ohm</li> <li>- Software Programmable Pull-up/Pull-down loads</li> <li>- Driver Memory: 64K behind each pin</li> </ul>	<p>Common Sensor Characteristics</p> <ul style="list-style-type: none"> <li>- 26 programmable dual sense thresholds from + 5.5 V to - 2.5 V. Sense thresholds independent of programmed drive level</li> <li>- Input impedance = 100 Kohm</li> <li>- Bit by bit compare and CRC capture modes</li> <li>- Sensor Memory: 64K behind each pin</li> </ul> <p>Clock Generation/Synchronization Characteristics</p> <ul style="list-style-type: none"> <li>- Clock Generation frequency programmable up to 20 MHz</li> <li>- Clock Synchronization frequency programmable up to 20 MHz</li> </ul>
Hardware Options	<ul style="list-style-type: none"> <li>- Test Points expandable to 7,680</li> <li>- Dedicated Accessory Slot:           <ul style="list-style-type: none"> <li>• Deep Serial Memory Instrument</li> <li>• Analog Functional Test Module</li> <li>• Vehicle Control Interface</li> <li>• System Frequency Test Module</li> <li>• Custom Function Board</li> <li>• Multi Protocol Instrument</li> </ul> </li> <li>- Flexible Power Supply Package - choose up to 14 power supplies from the following: 0-60 V @ 2.5 A, 0-20 V @ 8.0 A, 0-7 V @ 15 A</li> </ul>	<ul style="list-style-type: none"> <li>- Fixed Power Supplies: +5 V @ 6 A, <math>\pm 15</math> V @ 1 A or +5 V @ 6 A, <math>\pm 12</math> V @ 1.3 A</li> <li>- Ethernet Networking Interface (Thickwire/AUI)</li> <li>- Automation Ready Option</li> <li>- Power Tilt</li> <li>- Choice of multiplexed or non-multiplexed pin board options           <ul style="list-style-type: none"> <li>• TS121 (Non-Multiplexed - 3840 pins max, 3840 real pins)</li> <li>• TS124 (one-to-four multiplexing ratio - 3840 pins max, 960 real pins)</li> <li>• TS128 (one-to-eight multiplexing ratio - 3840 pins max, 480 real pins)</li> <li>• TS LX 128L (one-to-eight multiplexing ratio - 7680 pins max, 960 real pins)</li> <li>• TS LX 124L (one-to-four multiplexing ratio - 7680 pins max, 1920 real pins)</li> </ul> </li> </ul>
System Software	<ul style="list-style-type: none"> <li>- Windows XP Workstation</li> <li>- Test/Debug System Software License           <ul style="list-style-type: none"> <li>• Advanced AutoDebug</li> <li>• Program Explorer Graphical Real-Time Debug Station</li> <li>• Automatic Fault Grading</li> <li>• Test Execution Software</li> <li>• Panel-Test Software</li> <li>• Memory Bank Test Software</li> <li>• Diagnostic Software tools</li> <li>• Guided Probe</li> <li>• SoftProbe for automatic diagnosis of open input and output pins on digital ICs</li> <li>• BusBust isolates and diagnoses failures of ICs driving a common bus</li> </ul> </li> </ul>	<ul style="list-style-type: none"> <li>• Real-Time Data Collection</li> <li>• Flash ISP</li> <li>• Throughput Optimizer</li> <li>- SafeTest Technologies:           <ul style="list-style-type: none"> <li>• Low impedance pin driver enables testing of low voltage devices under backdrive conditions</li> <li>• 45mV sensor accuracy</li> <li>• Real-time backdrive current measurement reports</li> <li>• Programmable backdrive current and duration thresholds</li> <li>• Automatic driver verification guarantees pin drivers reach programmed thresholds</li> <li>• Fast test vector execution</li> <li>• Multi-Level Digital Isolation (MLDI) software</li> </ul> </li> </ul>
Software Options	<ul style="list-style-type: none"> <li>- Program Preparation License           <ul style="list-style-type: none"> <li>• Navigate Program Prep Environment</li> <li>• D2B Alchemist CAD preparation software</li> <li>• Analog, Digital, Boundary Scan and Mixed-Signal Device Libraries</li> <li>• Hybrid Test Generator for mixed-signal applications</li> <li>• Panel-Test Development Software</li> <li>• Flash ISP Development Software</li> <li>• Xpress Model for automatic device models of non-standard components</li> <li>• Circuit Analyzer-Based Test Generator</li> </ul> </li> <li>- Scan Pathfinder boundary scan test generation, execution, and diagnostics for boards with a mix of boundary scan and conventional devices</li> </ul>	<ul style="list-style-type: none"> <li>- BasicSCAN model generator for boundary scan devices</li> <li>- Graphical Repair Network</li> <li>- Junction Xpress vectorless test technique for detecting open device pins and marginal solder connections</li> <li>- Opens Xpress Vectorless Test Technique for detecting:           <ul style="list-style-type: none"> <li>• Open device pins</li> <li>• Open connector pins</li> <li>• Polarized capacitor orientation</li> <li>• Correct device orientation</li> </ul> </li> <li>- Powerful test program language for easy creation of custom tests</li> <li>- Optional Chinese Operator User Interface</li> </ul>



Because Technology Never Stops

Teradyne, Inc.  
 Assembly Test Division  
 600 Riverpark Drive  
 North Reading, MA 01864  
 +1.978.370.2700

www.teradyne.com/cbti

TestStation LX, UltraPin, SafeTest, SoftProbe and Bus Bust are trademarks of Teradyne, Inc.

All other brand and product names are trademarks or registered trademarks of their respective owners. Information contained in this document is summary in nature and subject to change without notice. Appearance of the final, delivered product may vary from the photographs shown herein.

© Teradyne 2004 • All rights reserved •

AT-169-1004